



PTO/SB/21 (12/97)

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(to be used for all correspondence after initial filing)

Application No. 09/475,104

Filing Date December 30, 1999

First Named Inventor Nagesh Vodrahalli

Group Art Unit 2811

Examiner Name Unassigned

Total Number of Pages in This Submission 5

Attorney Docket Number 42390P6785

ENCLOSURES (check all that apply)☒ Fee Transmittal Form☐ Fee Attached☒ Amendment / Response☐ After Final
☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/
Incomplete Application☐ Response to Missing
Parts under 37 CFR
1.52 or 1.53☐ Assignment Papers
(for an Application)☐ Drawing(s)☐ Licensing-related Papers☐ Petition Routing Slip (PTO/SB/69)
and Accompanying Petition☐ To Convert a
Provisional Application☐ Power of Attorney, Revocation
Change of Correspondence Address☐ Terminal Disclaimer☐ Small Entity Statement☐ Request for Refund

Remarks

☐ After Allowance Communication
to Group☐ Appeal Communication to Board
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(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☐ Additional Enclosure(s)
(please identify below):

TC 2800 MAIL ROOM

JUN - 7 2001

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENTFirm
or
Individual name

William W. Schaal, Reg. No. 39,018

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Signature

Date

May 31, 2001

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Our Docket No.: 042390P6785

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

VODRAHALLI, ET AL.

Application No.: 09/475,104

Filed: 12/30/99

For: High Performance Thermal Interface
Curing Process for Organic Flip Chip
Packages

Examiner: Alexander O. Williams

Art Group: 2826

RESPONSE TO THE OFFICE ACTION

Assistant Commissioner for Patents
Washington, DC 20231-9998

Sir:

In the Office Action dated May 2, 2001, the Examiner contends that Applicants claim two distinct inventions in the above-identified United States Patent Application; namely, a device for an integrated circuit package and a method for assembling an integrated circuit. Thus, pursuant to 35 U.S.C. § 121, the Examiner requires Applicants to restrict their Application to one of the alleged two inventions. In compliance with 35 U.S.C. § 121, Applicants elect the second group (claims 5-12) directed toward a method for assembling an integrated circuit.

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ABW

In view of the foregoing, Applicants request examination of the elected claims.

Examination at the Examiner's earliest convenience is respectfully solicited.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



WILLIAM W. SCHAAL

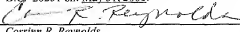
Reg. No. 39,018

Dated: May 31, 2001

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